

What is claimed is:

1. An information processing method for causing a computing device having a plurality of processors to carry out predetermined information processing,

the information processing method comprising:

a parallel processing block-forming step for dividing a program to be executed into a plurality of parallel processing blocks;

10       a thread-forming step for dividing said parallel processing blocks formed by said parallel processing block-forming step, into threads which are basic units to be assigned respectively to said plurality of processors for being processed thereby; and

15       an instructing step for instructing a predetermined processor to execute a next parallel processing block when said predetermined processor has terminated execution of said thread assigned thereto.

20       2. An information processing method according to claim 1, wherein when a predetermined instruction is given in said program to be executed, execution of a next parallel processing block is not instructed by said instructing step until processing of all of said threads 25 has been terminated.

3. A computer-readable recording medium which stores

a program to cause a computing device having a plurality of processors to carry out predetermined information processing,

      said program causing a computer to function as:

5       parallel processing block-forming means for dividing a program to be executed into a plurality of parallel processing blocks;

10      thread-forming means for dividing said parallel processing blocks generated by said parallel processing block-forming means, into threads which are basic units to be assigned respectively to said plurality of processors for being processed thereby; and

15      instructing means for instructing a predetermined processor to execute a next parallel processing block when said predetermined processor has terminated execution of said thread assigned thereto.

4.     An information processing system including a plurality of processors for carrying out predetermined information processing,

      the information processing system comprising:

      parallel processing block-forming means for dividing a program to be executed into a plurality of parallel processing blocks;

25      thread-forming means for dividing said parallel processing blocks generated by said parallel processing block-forming means, into threads which are basic units to

be assigned respectively to said plurality of processors for being processed thereby; and

instructing means for instructing a predetermined processor to execute a next parallel processing block when  
5 said predetermined processor has terminated execution of said thread assigned thereto.

5. A computer-readable recording medium which stores a program to cause a computing device having a plurality of  
10 processors to carry out predetermined information processing,

said program causing a computer to function as:

process-assigning means for generating a plurality of threads and assigning said threads to said processors for  
15 processing, respectively, when an execution request is made from a predetermined one of parallel processing blocks into which said program to be executed is divided; and

execution instructing means for instructing execution of a thread of a next parallel processing block by any of  
20 said processors when a corresponding of said threads has been processed.

6. A computer-readable recording medium according to claim 5, wherein when a predetermined instruction is given  
25 in said program to be executed, said execution instructing means does not instruct execution of a next parallel processing block until processing of all of said threads

**has been terminated.**

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